Fujitsu Siemens Computers
PRIMERGY TX300 S3, Intel Xeon processor X5355, 2.66 GHz

SPEC License #: 22
Tested by: Fujitsu Siemens Computers
Test date: Oct-2006
Hardware Avail: Jan-2007
Software Avail: May-2006

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Hardware

CPU: Intel Xeon processor X5355 (2.66 GHz, 2x4MB L2, 1333 MHz system bus)
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1.2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware: none

Software

Compiler: Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries),
SmartHeap Library Version 8.0 from http://www.microquill.com
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags
176.gcc: -Dallocal=alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
for C programs: -fast +FDO shlw32M.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib

Peak tuning flags
same as baseline (basepeak=true set globally)

The system bus runs at 1333 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.
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**Notes/Tuning Information (Continued)**

BIOS configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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