CFP2000 Result

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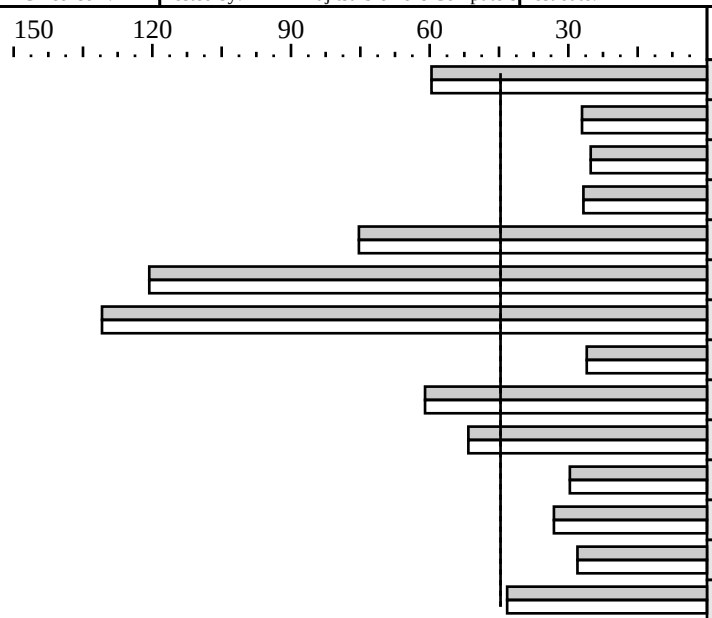
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECfp_rate2000 = 44.7

SPECfp_rate_base2000 = 44.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Oct-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	125	59.6	4	125	59.6
171.swim	4	532	27.0	4	532	27.0
172.mgrid	4	332	25.2	4	332	25.2
173.applu	4	364	26.7	4	364	26.7
177.mesa	4	86.3	75.3	4	86.3	75.3
178.galgel	4	112	121	4	112	121
179.art	4	92.2	131	4	92.2	131
183.equake	4	232	26.0	4	232	26.0
187.facerec	4	145	61.0	4	145	61.0
188.ammp	4	198	51.6	4	198	51.6
189.lucas	4	313	29.7	4	313	29.7
191.fma3d	4	294	33.1	4	294	33.1
200.sixtrack	4	182	28.0	4	182	28.0
301.apsi	4	279	43.2	4	279	43.2

Hardware

CPU:	Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)
Disk Subsystem:	Seagate ST336754SS (SAS, 15.4krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.1 for EM64T Build 20060816 (for 64-bit applications)
File System:	ext2
System State:	Multi-user runlevel 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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