CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

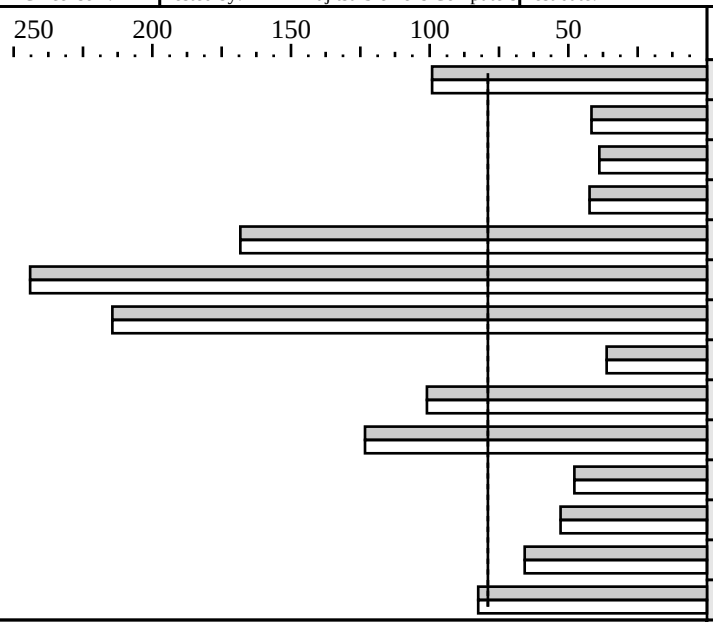
Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor E5320, 1.86 GHz

SPECfp_rate2000 = 79.0

SPECfp_rate_base2000 = 79.0

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Oct-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	150	99.1	8	150	99.1
171.swim	8	691	41.7	8	691	41.7
172.mgrid	8	430	38.8	8	430	38.8
173.applu	8	460	42.4	8	460	42.4
177.mesa	8	77.2	168	8	77.2	168
178.galgel	8	110	244	8	110	244
179.art	8	113	214	8	113	214
183.equake	8	333	36.2	8	333	36.2
187.facerec	8	175	101	8	175	101
188.ammp	8	166	123	8	166	123
189.lucas	8	388	47.8	8	388	47.8
191.fma3d	8	369	52.8	8	369	52.8
200.sixtrack	8	155	65.7	8	155	65.7
301.apsi	8	292	82.5	8	292	82.5

Hardware

CPU:	Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)
Disk Subsystem:	Seagate ST336754SS (SAS, 15.4krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.1 for EM64T Build 20060816 (for 64-bit applications)
File System:	ext2
System State:	Multi-user runlevel 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Hardware Prefetch = disable, Adjacent Sector Prefetch = disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries