CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

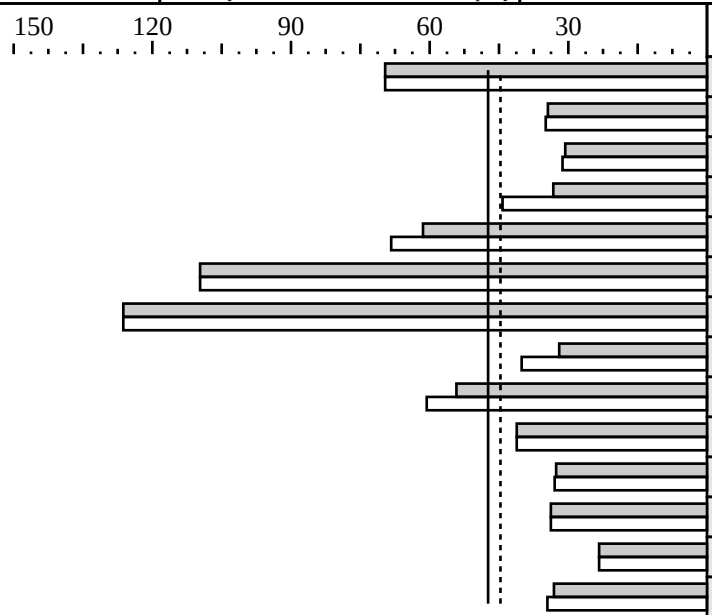
Hewlett-Packard Company

ProLiant BL460c (2.66GHz, Intel Xeon processor 5150)

SPECfp_rate2000 = 47.4

SPECfp_rate_base2000 = 44.7

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Oct-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	53.3	69.6	2	53.3	69.6
171.swim	2	209	34.4	2	206	34.9
172.mgrid	2	136	30.7	2	134	31.3
173.applu	2	147	33.3	2	110	44.2
177.mesa	2	52.9	61.4	2	47.5	68.3
178.galgel	2	61.3	110	2	61.3	110
179.art	2	47.8	126	2	47.8	126
183.equake	2	94.3	32.0	2	75.2	40.1
187.facerec	2	81.3	54.2	2	72.7	60.6
188.ammp	2	124	41.2	2	124	41.2
189.lucas	2	142	32.6	2	141	33.0
191.fma3d	2	144	33.8	2	144	33.8
200.sixtrack	2	109	23.3	2	109	23.3
301.apsi	2	182	33.1	2	175	34.5

Hardware

CPU:	Intel Xeon processor 5150 (2.66 GHz, 4 MB L2 shared, 1333 MHz bus)
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 GB (8x1 GB PC2-5300F)
Disk Subsystem:	2x72 GB 10 K SAS
Other Hardware:	

Software

Operating System:	RedHat Enterprise Linux 4.0 Advanced Server for AMD64/EM64T, Update 3 Kernel 2.6.9-34.ELsmp
Compiler:	Intel C++ Compiler for EM64T-based applications, (Version 9.1 Build 20060323) Intel Fortran Compiler for EM64T-based applications, (Version 9.1 Build 20060323) PathScale EKOPath(TM) Compiler Suite, Release 2.4
File System:	ext2
System State:	Multi-user run level 3

Notes/Tuning Information

```
+FDO: PASS1= -prof_gen  PASS2=-prof_use (Intel Compiler)
+FDO: PASS1= -fb_create fbdata PASS2=-fb_opt fbdata (PathScale Compiler)
ifort is the Intel Fortran compiler, icc is the Intel C++ compiler; and
pathf95 is PathScale Fortran compiler, pathcc is the PathScale C compiler.
Base tuning for C programs:  icc -fast -auto_ilp32 +FDO
Base tuning for FORTRAN programs:  ifort -fast +FDO
Portability:
  -DSPEC_CPU2000_LP64 applied to all benchmarks
  178.galgel:   -FI
Peak tuning:
  168.wupwise:  basepeak=1
  171.swim:     pathf95 -Ofast -LNO:fusion=2:simd=0 -WOPT:val=0 -march=em64t
  172.mgrid:    pathf95 -Ofast -CG:load_exe=0 -LNO:blocking=off:prefetch_ahead=5
                -OPT:ro=3:unroll_size=256 -WOPT:mem_opnds=on -march=em64t
  173.applu:    pathf95 -O3 -ipa -CG:load_exe=0
                -LNO:fission=1:fusion=2:blocking=off:full_unroll_size=9000
                -OPT:IEEE_a=3:ro=3 -TENV:X=3 -march=em64t
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
    177.mesa:     pathcc -O2 -ipa -OPT:Ofast -fno-math-errno -CG:local_fwd_sched=on
                  -GRA:optimize_boundary=on -march=em64t +FDO
    178.galgel:   basepeak=1
    179.art:      basepeak=1
    183.equake:   icc -fast +FDO ONESTEP=yes -rcd -auto-ilp32
    187.facerec:  pathf95 -Ofast -IPA:plimit=1500 -LNO:fusion=2
                  -OPT:IEEE_NaN_Inf=off:ro=3:unroll_size=0 -march=em64t +FDO
    188.ammp:     basepeak=1
    189.lucas:    ifort -fast ONESTEP=yes
    191.fma3d:    basepeak=1
    200.sixtrack: basepeak=1
    301.apsi:     pathf95 -Ofast -CG:load_exe=0 -LNO:opt=0:prefetch=1 -march=em64t
  BIOS Configuration Notes
    Power Regulator set to Static High Performance Mode
```