CINT2000 Result

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Hewlett-Packard Company

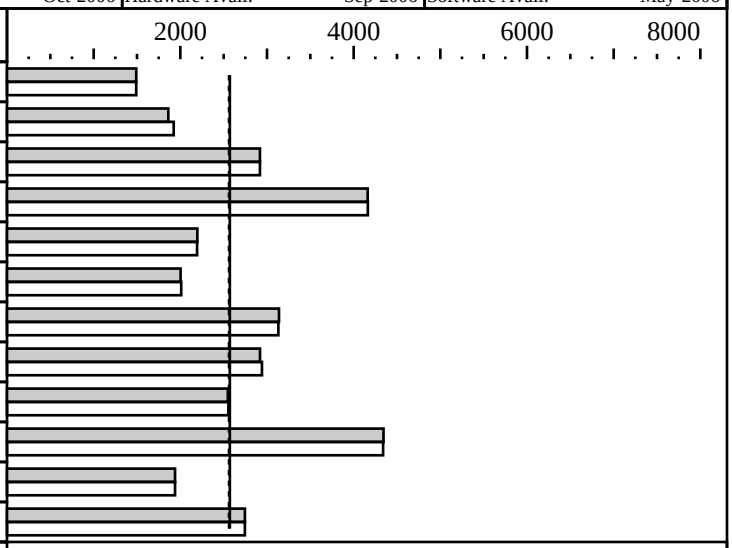
ProLiant ML310 G4 (2.4 GHz, Intel Xeon processor 3060)

SPECint2000 = 2571

SPECint_base2000 = 2562

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Oct-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	93.9	1491	93.9	1491
175.vpr	1400	75.2	1861	72.8	1924
176.gcc	1100	37.7	2919	37.7	2919
181.mcf	1800	43.3	4162	43.2	4163
186.crafty	1000	45.5	2196	45.6	2193
197.parser	1800	89.9	2003	89.5	2010
252.eon	1300	41.4	3138	41.5	3131
253.perlbmk	1800	61.7	2919	61.2	2942
254.gap	1100	43.2	2548	43.1	2554
255.vortex	1900	43.7	4344	43.8	4340
256.bzip2	1500	77.4	1939	77.4	1939
300.twolf	3000	109	2745	109	2745



Hardware

CPU:	Intel Xeon processor 3060 (2.4GHz, 4MB L2 shared, 1066 MHz bus)
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 GB (2x2 GB PC2-5300E)
Disk Subsystem:	1x80 GB SATA150
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

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Notes/Tuning Information (Continued)

```
  300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
   Power Regulator set to Static High Performance Mode
```