CFP2000 Result

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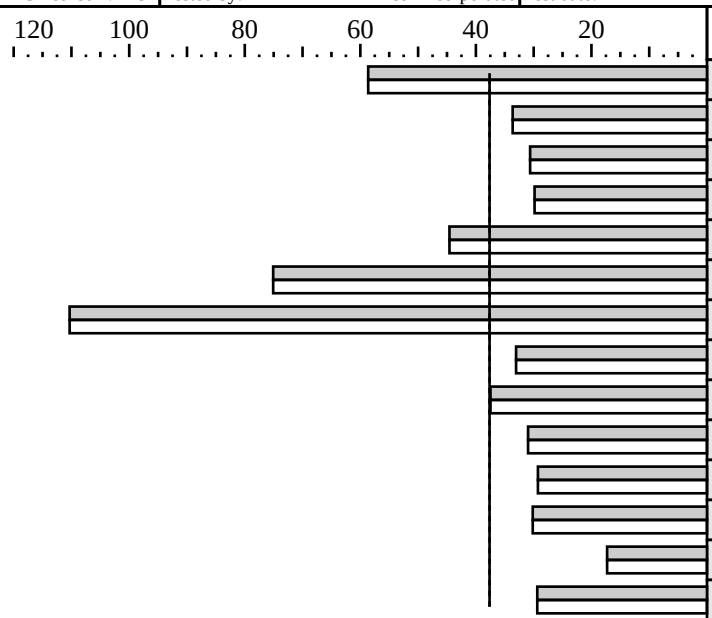
Acer Incorporated

Acer Altos R520 (3.73 GHz Intel Xeon Processor 5080)

SPECfp_rate2000 = 37.6

SPECfp_rate_base2000 = 37.6

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Nov-2006 | Software Avail: Dec-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	63.3	58.6	2	63.3	58.6
171.swim	2	214	33.6	2	214	33.6
172.mgrid	2	136	30.6	2	136	30.6
173.applu	2	163	29.8	2	163	29.8
177.mesa	2	72.9	44.6	2	72.9	44.6
178.galgel	2	89.6	75.1	2	89.6	75.1
179.art	2	54.7	110	2	54.7	110
183.equake	2	91.3	33.0	2	91.3	33.0
187.facerec	2	118	37.5	2	118	37.5
188.ammp	2	165	31.0	2	165	31.0
189.lucas	2	159	29.3	2	159	29.3
191.fma3d	2	162	30.2	2	162	30.2
200.sixtrack	2	148	17.3	2	148	17.3
301.apsi	2	205	29.4	2	205	29.4

Hardware

CPU:	Intel Xeon Processor 5080 (3.73 GHz, 2x2MB L2, 1066MHz system bus)
CPU MHz:	3733
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM 667
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Red Hat Enterprise Linux 4 Advanced Server Update 3 EM64T (Kernel 2.6.9-34.ELsmp)
Compiler:	Intel C++ Compiler 9.0 Build 20051201 for EM64T Intel Fortran Compiler 9.0 Build 20051201 for EM64T
File System:	ext3
System State:	Runlevel 3

Notes/Tuning Information

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GENERAL
 ONESTEP=yes for all benchmarks
 +FDO :  PASS1= -prof_gen   PASS2= -prof_use
PORTABILITY FLAGS
 -DSPEC_CPU2000_LP64 applied to all benchmarks
 178.galgel: -FI for fixed-format Fortran
BASE TUNING
 Baseline optimizations for C and Fortran: -fast +FDO
PEAK TUNING
 basepeak=yes set for all benchmarks
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
This result was measured on the Acer Alto R720.
The Altos R520 and Altos R720 are electronically equivalent.
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