CINT2000 Result

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Hewlett-Packard Company

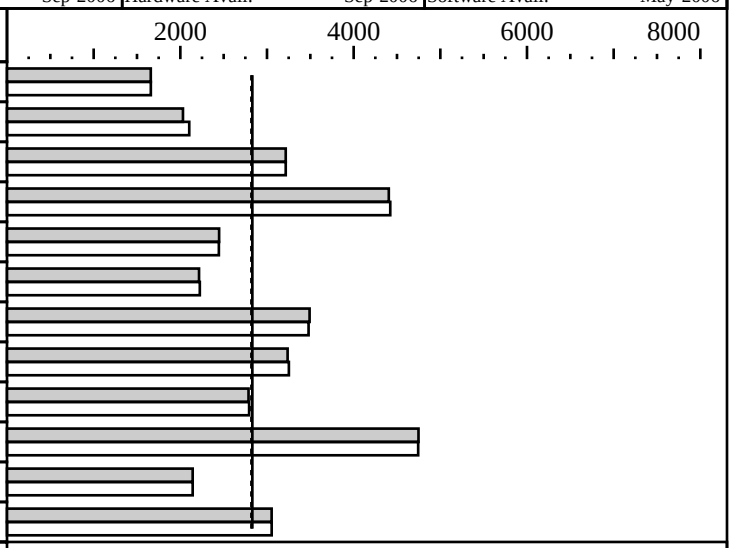
ProLiant DL320 G5 (2.66 GHz, Intel Xeon processor 3070)

SPECint2000 = 2831

SPECint_base2000 = 2820

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Sep-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	84.4	1659	84.4	1660
175.vpr	1400	69.0	2030	66.6	2103
176.gcc	1100	34.2	3216	34.2	3216
181.mcf	1800	40.9	4405	40.7	4422
186.crafty	1000	40.9	2447	40.9	2445
197.parser	1800	81.2	2216	80.9	2225
252.eon	1300	37.2	3491	37.4	3480
253.perlbmk	1800	55.6	3238	55.3	3252
254.gap	1100	39.5	2787	39.4	2794
255.vortex	1900	40.0	4748	40.0	4744
256.bzip2	1500	70.0	2142	70.0	2142
300.twolf	3000	98.3	3053	98.2	3054



Hardware

CPU:	Intel Xeon processor 3070 (2.66 GHz, 4 MB L2 shared, 1066 MHz bus)
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 GB (4x2 GB PC2-5300E)
Disk Subsystem:	1x250 GB SATA
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
```

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Notes/Tuning Information (Continued)

```
    256.bzip2:    basepeak=1
    300.twolf:    -fast                +FDO              shlw32M.lib
  BIOS Configuration Notes
    Power Regulator set to Static High
```