CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

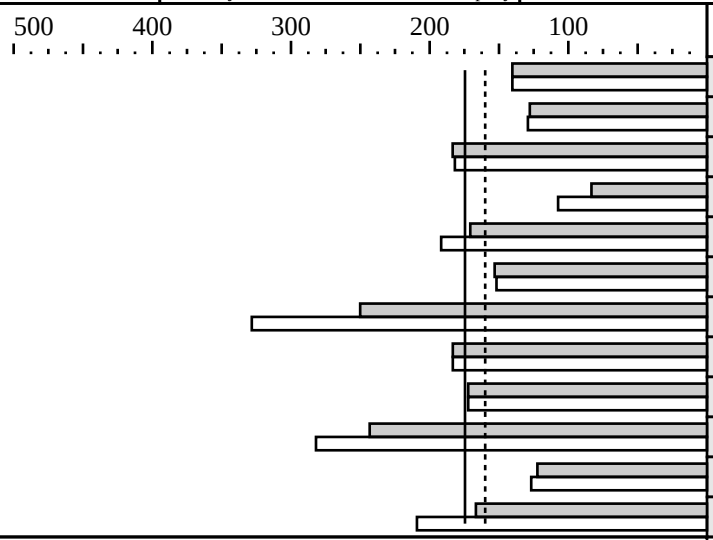
Hewlett-Packard Company

ProLiant DL585 G2 (AMD Opteron (TM) 8220SE)

SPECint_rate2000 = 175

SPECint_rate_base2000 = 160

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Sep-2006 | Hardware Avail: Sep-2006 | Software Avail: Apr-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	92.6	140	8	92.6	140
175.vpr	8	102	128	8	101	129
176.gcc	8	55.7	183	8	56.1	182
181.mcf	8	200	83.3	8	156	107
186.crafty	8	54.4	171	8	48.4	192
197.parser	8	109	153	8	110	152
252.eon	8	48.2	250	8	36.8	328
253.perlbmk	8	91.1	183	8	91.1	183
254.gap	8	59.3	172	8	59.3	172
255.vortex	8	72.5	243	8	62.5	282
256.bzip2	8	114	122	8	110	127
300.twolf	8	167	167	8	133	209

Hardware

CPU:	AMD Opteron (TM) 8220SE
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip
CPU(s) orderable:	2,4 chips
Parallel:	No
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	32 GB (16x2 GB PC2-5300P CL5.0)
Disk Subsystem:	1x36 GB 10 K SAS
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast -arch:SSE2 -Oi- +FDO ONESTEP=yes
Base tuning for C++ programs: -fast -Qcxx_features -arch:SSE2 +FDO ONESTEP=yes
Portability flags:
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:    basepeak=yes
  175.vpr:     -fast -arch:SSE2 -Qoption,c,-ip_ninl_max_stats=2000
               -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes
  176.gcc:     -fast -arch:SSE2 -Oi- -Qunroll3          +FDO
  181.mcf:     -O3 -Qipo -QaxN                         +FDO ONESTEP=yes shlw32M.lib
  186.crafty:  -fast -arch:SSE2                         +FDO ONESTEP=yes
  197.parser:  -arch:SSE2 -Oi- -Qipo                    +FDO ONESTEP=yes
  252.eon:     -fast -arch:SSE2 -Qansi_alias -Qoption,c,-ip_ninl_max_stats=2000
               -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes
  253.perlbmk: basepeak=yes
  254.gap:     basepeak=yes
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
  255.vortex:  -fast -arch:SSE2 -Oi- -Qoption,c,-ip_ninl_max_stats=2000
               -Qoption,c,-ip_ninl_max_total_stats=4500 +FDO ONESTEP=yes shlw32M.lib
  256.bzip2:   -O3 -Qipo -QaxN  -Qunroll2                              shlw32M.lib
  300.twolf:   -fast -arch:SSE2 -Qansi_alias           +FDO ONESTEP=yes shlw32M.lib
BIOS Configuration Notes
  Node Interleaving Disabled
Other Configuration Notes
  The start /b /wait /affinity command is used to bind CPU(s) to processes.
```