Fujitsu Siemens Computers
PRIMERGY RX600 S3, Intel Xeon processor 7120M, 3.0 GHz

SPECint_rate2000 = 119
SPECint_rate_base2000 = 119

CPU: Intel Xeon processor 7120M (3.0 GHz, 2x1MB L2, 4MB L3, 800 MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2,4 chips
Parallel: No
Primary Cache: 12k micro-ops(I) + 16KB(D) on chip, per core
Secondary Cache: 1024KB(I+D) on chip, per core
L3 Cache: 4MB on chip, per chip
Other Cache: N/A
Memory: 16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)
Disk Subsystem: Fujitsu MAS3367NC (SCSI 15krpm, 36GB)
Other Hardware: none

Software
Compiler: Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries),
SmartHeap Library Version 8.0 from http://www.microquill.com
File System: NTFS
System State: Default

Notes/Tuning Information
GENERAL
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Portability flags
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Base tuning flags
for C programs: -fast +FDO shlW32M.lib
for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Peak tuning flags
same as baseline (basepeak=true set globally)
The system bus runs at 800 MHz
This result was measured with 32-bit binaries using the 32-bit version of the operating system.
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Notes/Tuning Information (Continued)

This result was measured on the PRIMERGY TX600 S3. The PRIMERGY TX600 S3 and the PRIMERGY RX600 S3 are electronically equivalent.

BIOS Configuration:
Adjacent Sector Prefetch = Disable

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