CFP2000 Result

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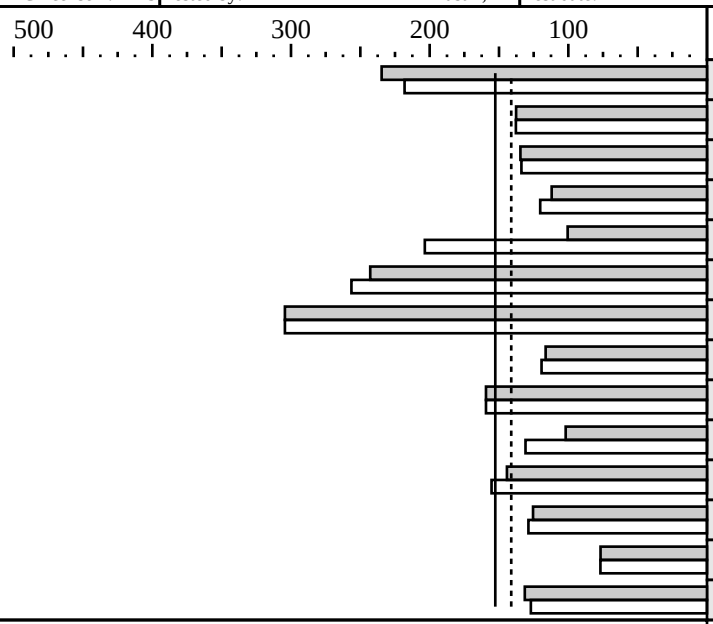
Advanced Micro Devices

Tyan Thunder K9QE (S4985) AMD Opteron (TM) 8220SE

SPECfp_rate2000 = 153

SPECfp_rate_base2000 = 141

SPEC license #: 49 | Tested by: AMD Austin, TX | Test date: Jul-2006 | Hardware Avail: Sep-2006 | Software Avail: Oct-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	63.3	235	8	68.1	218
171.swim	8	209	138	8	209	138
172.mgrid	8	124	135	8	125	134
173.applu	8	174	112	8	162	120
177.mesa	8	129	100	8	63.8	203
178.galgel	8	111	243	8	105	256
179.art	8	79.3	304	8	79.3	304
183.equake	8	104	116	8	101	119
187.facerec	8	111	159	8	111	159
188.ammp	8	200	102	8	156	131
189.lucas	8	129	144	8	119	155
191.fma3d	8	155	125	8	151	129
200.sixtrack	8	133	76.8	8	133	76.9
301.apsi	8	184	131	8	190	127

Hardware

CPU:	AMD Opteron (TM) 8220SE
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip
CPU(s) orderable:	1,2,4
Parallel:	no
Primary Cache:	64KBI + 64KBD on chip per core
Secondary Cache:	1024KB (I+D) on chip per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16x512MB, DDR2-667 CL4 ECC Reg
Disk Subsystem:	SATA, 74Gb
Other Hardware:	None

Software

Operating System:	Windows Server 2003 SP1 (32-bit)
Compiler:	Intel C++ 9.0 build 20050912Z for IA32, Intel Fortran 9.0 build 20050912Z for IA32, Microsoft Visual Studio .NET 7.0.9466 (libraries) PGI Fortran compiler 6.0-5 for Windows XP, PGI C compiler 6.0-5 for Windows XP, ACML Version 2.5.3 (bundled with PGI 6.0-5)
File System:	NTFS
System State:	default

Notes/Tuning Information

```
+FDO:
 icl, ifort  : PASS1=-Qprof_gen  PASS2=-Qprof_use
 pgf90       : PASS1=-Mpfi       PASS2=-Mpfo
ifort is the Intel Fortran compiler, icl is the Intel C++ compiler and
pgf90 is the PGI Fortran 90 compiler.
pgcc is the PGI C compiler.
ONESTEP is set to 1 for every compile with the PGI compilers.
Portability:
178.galgel:                 -Mfixed
Baseline: C      :  pgcc    -fastsse -Mipa=fast,inline
Baseline: Fortran:  pgf90   -fastsse -Mipa=fast,inline +FDO
Peak tuning:
168.wupwise:        pgf90   -fastsse -Mipa=fast,inline -Mnovect
171.swim:           ifort   -Qipo  -O3  -QaxN -QxW -Qunroll0 +FDO
172.mgrid:          pgf90   -fastsse -Mipa=fast,inline
173.applu:          ifort   -Qipo  -O3 -QaxN -QxW -auto +FDO
177.mesa:           icl     -Qipo -QxW -Qunroll1 -Qansi_alias +FDO
```

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Notes/Tuning Information (Continued)

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                        -Qoption,c,-ip_ninl_max_stats=1500,-ip_ninl_max_total_stats=4500
 178.galgel:         pgf90   -fastsse -Mipa=fast,safe -Munix -lacml
                             RM_SOURCES=lapak.f90
 179.art:            pgcc     basepeak=yes
 183.equake:         icl     -O3 -Qipo -QxW +FDO
 187.facerec:        pgf90   basepeak=1
 188.ammp:           icl     -Oa   -QxW  -Zp4 -Qansi_alias
 189.lucas:          ifort   -Qipo -QxW -Qunroll1
 191.fma3d:          pgf90   -Mipa=fast,inline -fastsse -Mnovect +FDO
 200.sixtrack:       pgf90   -fastsse -Mipa=fast,inline
 301.apsi:           pgf90   -fastsse -Mipa=fast,inline
All memory slots populated on all CPU(s)
'start /b /wait /affinity' command is used to bind CPU(s) to processes
The system can be built using a Zippy 700W EPS12V power supply and any SSI-MEB case.
BIOS Changes: Enable Basic ECC
```