Hewlett-Packard Company
HP Integrity Superdome (1.6GHz/24MB Dual Core Itanium 2)

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<th>Benchmark</th>
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Hardware
- CPU: Dual Core Intel(R) Itanium(R) 2 (1.6GHz/24MB, 533MHz FSB)
- CPU MHz: 1600
- FPU: Integrated
- CPU(s) enabled: 128 cores, 64 chips, 2 cores/chip (Hyper-Threading Technology disabled)
- CPU(s) orderable: 2-128
- Parallel: no
- Primary Cache: 16KBI + 16KBD on chip, per core
- Secondary Cache: 1024KBI + 256KBD on chip, per core
- L3 Cache: 12.0MB (I+D) on chip, per core
- Other Cache: N/A
- Memory: 512GB (512x1GB DIMMs)
- Disk Subsystem: 8x36GB 15K SCSI (striped)
- Other Hardware: N/A

Software
- Operating System: HPUX11i-TCOE B.11.23.0606
- Compiler: HP C/ANSI C Compiler C.11.23.12
- HP aC++ Compiler C.11.23.12
- File System: vxfs
- System State: Multi-user

Notes/Tuning Information
- Portability Flags
  176.gcc : -DHOST_WORDS_BIG_ENDIAN
  186.crafty : -DHP
  253.perlbmk : -DSPEC_CPU2000_HP
  254.gap : -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO

- Base Flags
  all : +Oprofile=collect:all/+Oprofile=use
  C : +Ofaster +Otype_safety=ansi
  C++ : +Ofaster +Otype_safety=ansi

- Peak Flags:
  same as base (basepeak=true set globally)

- Kernel Tunables:
  dbc_max_pct=10
db_c_min_pct=10
maxdsiz=3221225472
maxssiz=401604608
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SPECint_rate2000 = 2367
SPECint_rate_base2000 = 2367

Notes/Tuning Information (Continued)

maxdsiz_64bit=4396972765184
maxssiz_64bit=1073741824
vps_chatr_ceiling=4194304

Notes:

The system had the June 2006 HP-UX 11i v2 Technical Computing
Operating Environment (TCOE) installed, along with the AR0606
compilers, and the following patches:

PHSS_34858 linker + fdp cumulative patch
PHSS_34853 Math Library Cumulative Patch
PHSS_34854 Integrity Unwind Library
PHSS_34855 HP C Compiler (A.06.12)
PHSS_34856 aC++ Compiler (A.06.12)
PHSS_34857 u2comp/be/plugin library patch
PHSS_34395 FORTRAN I/O Library [libIO77]
PHSS_34397 FORTRAN Intrinsics [libF90 B.11.23.16]
PHSS_34399 HP FORTRAN90
PHSS_34441 aC++ Runtime (IA: A.06.10)
PHKL_34020 Perfmon enhancements and Itanium-2 9000 HWE

System was configured as a single partition with 16 cells and
4 processors (8 cores) per cell. Memory was configured as 50%
local and 50% interleaved.

HP-UX mpsched utility was used to assign processes to cells.

filesystem used for spec runs mounted tmplog