CINT2000 Result

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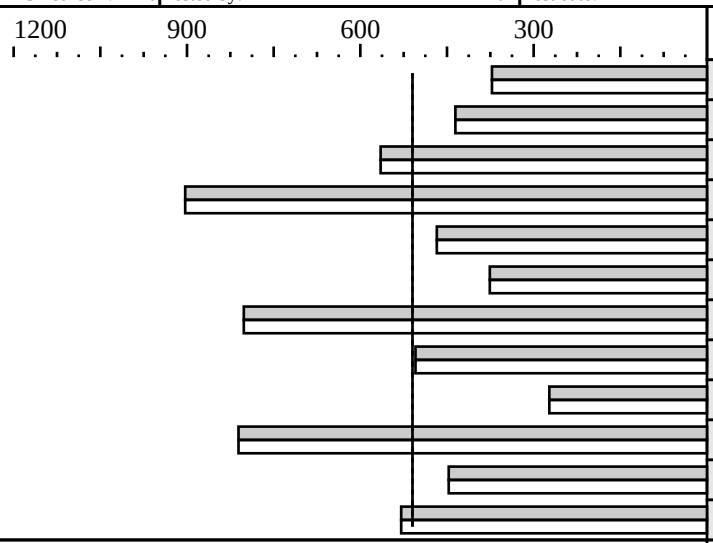
Bull
NovaScale 5325 (1600MHz)

SPECint_rate2000 = 510

SPECint_rate_base2000 = 510

SPEC license #: 20 | Tested by: Bull | Test date: Jun-2006 | Hardware Avail: Aug-2006 | Software Avail: Jun-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	32	140	372	32	140	372
175.vpr	32	119	435	32	119	435
176.gcc	32	72.3	565	32	72.3	565
181.mcf	32	74.0	903	32	74.0	903
186.crafty	32	79.4	468	32	79.4	468
197.parser	32	178	376	32	178	376
252.eon	32	60.2	802	32	60.2	802
253.perlbmk	32	132	505	32	132	505
254.gap	32	150	273	32	150	273
255.vortex	32	87.0	811	32	87.0	811
256.bzip2	32	125	447	32	125	447
300.twolf	32	210	529	32	210	529

Hardware

CPU:	Itanium 2 processor 9040 1600 MHz FSB 400MHz
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	32 cores, 16 chips, 2 cores/chip, (Hyper-Threading Technology disabled)
CPU(s) orderable:	4, 8, 16 or 32 (chips)
Parallel:	No
Primary Cache:	16KBI + 16KBD on chip per core
Secondary Cache:	1MBI + 256KBD on chip per core
L3 Cache:	9MB (I+D) on chip per core
Other Cache:	N/A
Memory:	128 GB (128* 1GB DIMMs DDR2 400MHZ PC3200 CL3)
Disk Subsystem:	2*10krpm 73GB SCSI disks
Other Hardware:	

Software

Operating System:	Bull Advanced Server 4 (linux kernel 2.6.12 (64k pages), glibc 2.3.4)
Compiler:	Intel(R) C++ Compiler for Linux 9.1 (Build 20060523)
File System:	ext3
System State:	Multi User

Notes/Tuning Information

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+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -fast -auto_ilp32 +FDO
  C++ programs:     -fast -ansi_alias +FDO

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST  -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Peak optimization flags: basepeak=true

Processes were bound to CPUs using pexec
```

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Notes/Tuning Information (Continued)

8 FSB boxes with 2 CPU chips/FSB, and 16 DIMMs/FSB
For information about Bull please see:
http://www.bull.com