CINT2000 Result

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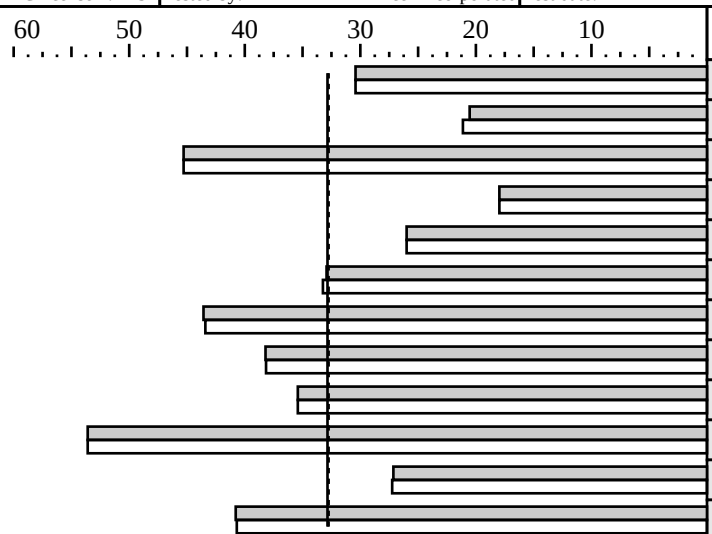
Acer Incorporated

Acer Altos R720 (3.0 GHz Intel Xeon Processor 5050)

SPECint_rate2000 = 32.8

SPECint_rate_base2000 = 32.7

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Jun-2006 | Software Avail: Nov-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	214	30.4	4	214	30.4
175.vpr	4	316	20.5	4	308	21.1
176.gcc	4	113	45.3	4	113	45.3
181.mcf	4	465	18.0	4	465	18.0
186.crafty	4	179	26.0	4	179	26.0
197.parser	4	254	32.9	4	251	33.2
252.eon	4	138	43.6	4	139	43.4
253.perlbmk	4	219	38.2	4	219	38.2
254.gap	4	144	35.4	4	144	35.4
255.vortex	4	165	53.6	4	165	53.6
256.bzip2	4	256	27.1	4	255	27.2
300.twolf	4	341	40.8	4	342	40.7

Hardware

CPU:	Intel Xeon Processor 5050 (3.0 GHz, 2x2MB L2, 667MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM 533
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler:	Intel C++ Compiler 9.0 Build 20051130Z for IA32 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:   -fast -Oa -Qunroll1 +FDO
```

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Notes/Tuning Information (Continued)

```
300.twolf:    -fast  +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
```