CINT2000 Result

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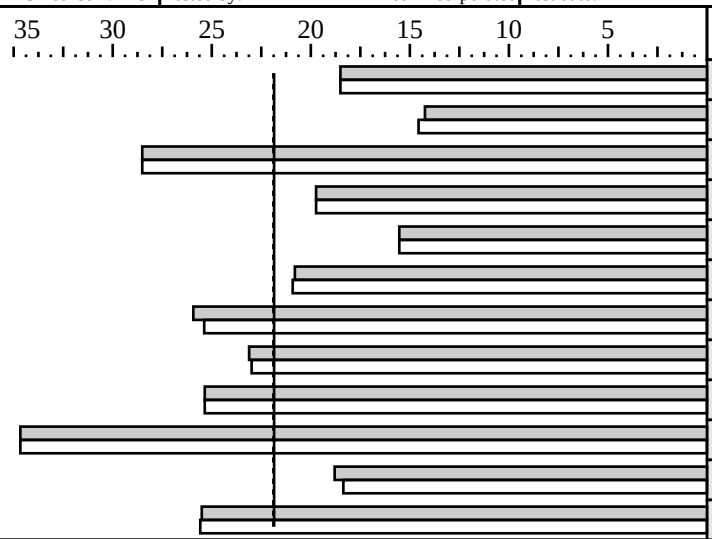
Acer Incorporated

Acer Altos R710 (3.6 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint_rate2000 = 21.9

SPECint_rate_base2000 = 21.9

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Apr-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	176	18.5	2	176	18.5
175.vpr	2	228	14.2	2	223	14.6
176.gcc	2	89.5	28.5	2	89.5	28.5
181.mcf	2	212	19.7	2	212	19.7
186.crafty	2	149	15.5	2	149	15.5
197.parser	2	201	20.8	2	200	20.9
252.eon	2	116	25.9	2	119	25.4
253.perlbmk	2	181	23.1	2	182	23.0
254.gap	2	101	25.4	2	101	25.4
255.vortex	2	127	34.7	2	127	34.7
256.bzip2	2	185	18.8	2	190	18.4
300.twolf	2	273	25.5	2	272	25.6

Hardware

CPU:	Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Enable)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 9.0 Build 20050430Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast  +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```