# CINT2000 Result

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## SuperMicro

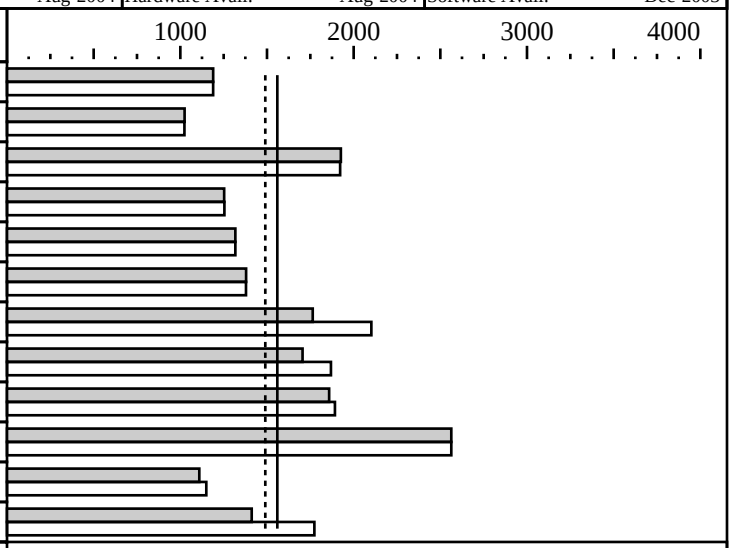
P8SAA ( 3.6 GHz Pentium 4, 1MB L2 Cache)

SPECint2000 = 1560

SPECint_base2000 = 1490

SPEC license #01176 | Tested by: SuperMicro Inc., San Jose, CA | Test date: Aug-2004 | Hardware Avail: Aug-2004 | Software Avail: Dec-2003

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 164.gzip | 1400 | 118 | 1189 | 118 | 1189 |
| 175.vpr | 1400 | 137 | 1025 | 137 | 1024 |
| 176.gcc | 1100 | 57.1 | 1926 | 57.2 | 1922 |
| 181.mcf | 1800 | 144 | 1253 | 144 | 1254 |
| 186.crafty | 1000 | 75.9 | 1317 | 75.9 | 1317 |
| 197.parser | 1800 | 130 | 1379 | 131 | 1379 |
| 252.eon | 1300 | 73.7 | 1764 | 61.8 | 2102 |
| 253.perlbmk | 1800 | 106 | 1705 | 96.3 | 1869 |
| 254.gap | 1100 | 59.2 | 1859 | 58.1 | 1892 |
| 255.vortex | 1900 | 74.1 | 2563 | 74.1 | 2563 |
| 256.bzip2 | 1500 | 135 | 1109 | 130 | 1150 |
| 300.twolf | 3000 | 213 | 1412 | 169 | 1773 |




### Hardware

| | |
|---|---|
| CPU: | Intel Pentium 4 ( 3.6 GHz, 800 MHz system bus) |
| CPU MHz: | 3600 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip (HT Technology Enabled) |
| CPU(s) orderable: | 1 |
| Parallel: | No |
| Primary Cache: | 12k micro-ops I + 16KBD on chip |
| Secondary Cache: | 1024KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 4 x 512MB PC 4300 DDR2 SDRAM |
| Disk Subsystem: | 1 x 200GB ATA133 IDE, 7200 RPM |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Windows 2003 Enterprise Server |
| Compiler: | Intel C/C++ Compiler 8.0 (20031229Z)<br>Microsoft Visual Studio .Net 7.0.9466(for libraries)<br>SmartHeap Library Version 7.1 from http://www.microquill.com/ |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -QxN -fast -Qansi_alias +FDO
Base tuning for C++ programs: -QxN -fast -GX -GR
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     -QxN -fast -Qansi_alias -Oa  +FDO
175.vpr:      -QxN -fast -Qansi_alias +FDO
176.gcc:      -QxN -fast -Qansi_alias +FDO
181.mcf:      -QxN -fast -Qansi_alias +FDO
186.crafty:   -QxN -fast -Qansi_alias -Oa +FDO
197.parser:   -QxN -fast -Qansi_alias  +FDO
252.eon:      -QxN -O2 -Qipo +FDO
253.perlbmk:  -QxN -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      -QxN -fast -Qunroll1 -Zp8 +FDO
255.vortex    basepeak=yes
256.bzip2:   -fast -Oa -Qunroll1 +FDO
```

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## Notes/Tuning Information (Continued)

300.twolf: -QxN -fast -O3 +FDO shlW32M.lib