CINT2000 Result

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SGI

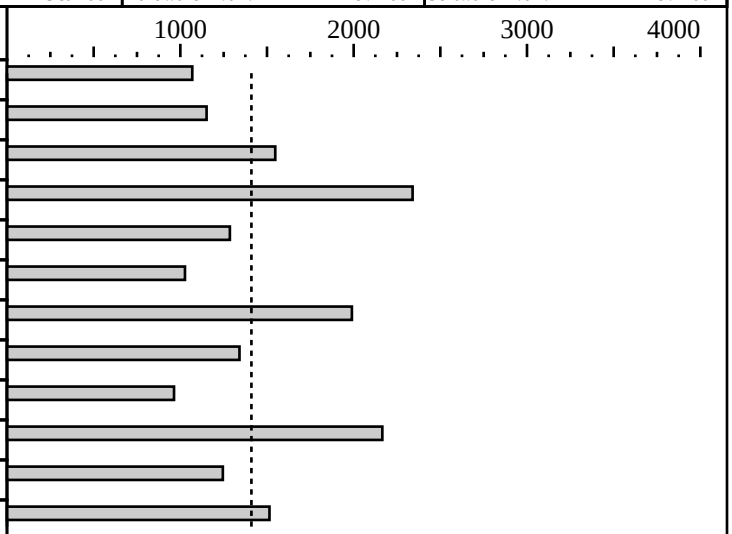
SGI Altix 3700 Bx2 (1600MHz 6M L3, Itanium 2)

SPECint2000 = --

SPECint_base2000 = 1410

SPEC license #: 4 | Tested by: SGI | Test date: Oct-2004 | Hardware Avail: Nov-2004 | Software Avail: Nov-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	131	1069		
175.vpr	1400	122	1151		
176.gcc	1100	71.1	1548		
181.mcf	1800	76.9	2340		
186.crafty	1000	77.8	1286		
197.parser	1800	175	1027		
252.eon	1300	65.3	1990		
253.perlbmk	1800	134	1341		
254.gap	1100	114	964		
255.vortex	1900	87.7	2165		
256.bzip2	1500	120	1245		
300.twolf	3000	198	1514		



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	16-256
Parallel:	No
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB (I+D) on chip, per core
L3 Cache:	6.0MB (I+D) on chip, per core
Other Cache:	N/A
Memory:	4 GB (512MB PC2700 DIMMS)
Disk Subsystem:	8 x 73 GB FibreChannel (Seagate Cheetah 10k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v3.0 Service Pack 2
Compiler:	Intel(R) C++ Compiler for Linux 8.1 (Build 20041021) MicroQuill SmartHeap Library 7.01 (www.microquill.com)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

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+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -fast -auto_ilp32 +FDO
  C++ programs:     -fast -ansi_alias +FDO
  Extra Libraries:  libsmartheap64.a

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64 srcalt=closed_files

Extra CPUs were disabled at firmware.
Processes were bound to CPUs using dplace.
```