CINT2000 Result

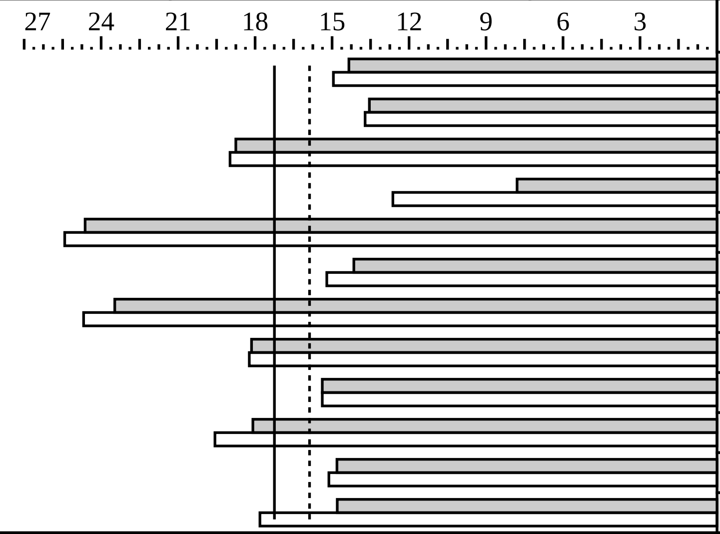
Advanced Micro Devices

ASUS SK8V Motherboard, AMD Opteron (TM) 150

SPECint_rate2000 = 17.2

SPECint_rate_base2000 = 15.9

SPEC license #:	Tested by:	Test date:	Hardware Avail:	Software Avail:
49	AMD, Austin, TX	Apr-2004	Jun-2004	Apr-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	113	14.3	1	109	14.9
175.vpr	1	120	13.5	1	118	13.7
176.gcc	1	68.0	18.8	1	67.2	19.0
181.mcf	1	268	7.79	1	165	12.6
186.crafty	1	47.1	24.6	1	45.6	25.4
197.parser	1	148	14.2	1	137	15.2
252.eon	1	64.3	23.5	1	61.1	24.7
253.perlbmk	1	115	18.1	1	115	18.2
254.gap	1	83.0	15.4	1	83.0	15.4
255.vortex	1	122	18.1	1	113	19.6
256.bzip2	1	117	14.8	1	115	15.1
300.twolf	1	235	14.8	1	195	17.8

Hardware

CPU:	AMD Opteron (TM) 150
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x512 MB PC3200 DDR SDRAM ECC Reg CL2
Disk Subsystem:	IDE, Western Digital WD2000
Other Hardware:	None

Software

Operating System:	SuSE Linux 8.0 SLES 64 bit (SP3)
Compiler:	SuSE optional gcc 3.3 compiler (20030312) (from SLES8 SP3)
File System:	Linux/ext3
System State:	Multi-user SuSE Run level 3

Notes/Tuning Information

```
+FDO: PASS1=-fprofile-arcs PASS2=-fbranch-probabilities
Portability:
              PORTABILITY=-DSPEC_CPU2000_LP64 is applied to all benchmarks,
              except for peak runs of 181.mcf, 197.parser and 300.twolf, which
              are compiled for 32-bit in peak.
 186.crafty:  CPORTABILITY=-DLINUX_i386
 176.gcc:     Approved src.alt is used
 252.eon:     CXXPORTABILITY=-DHAS_ERRLIST -DFMAX_IS_DOUBLE
              Approved src.alt is used.
 253.perlbmk: CPORTABILITY=-DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL
                           -DSPEC_CPU2000_GLIBC22
 254.gap:     CPORTABILITY=-DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO
                         -DSYS_HAS_SIGNAL_PROTO -DSYS_HAS_ANSI -DSYS_HAS_CALLOC_PROTO
Baseline C:   gcc -O3 -funroll-all-loops +FDO
Baseline C++: g++ -O3 -funroll-all-loops +FDO
Peak tuning:
 164.gzip:    -O3 -funroll-all-loops -finline-limit=900 -freduce-all-givs +FDO
 175.vpr:     -O3 -funroll-loops -finline-limit=1000 +FDO
 176.gcc:     -O3 -funroll-all-loops -finline-limit=900 +FDO
 181.mcf:     -O3 -funroll-all-loops -m32 +FDO
```

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Notes/Tuning Information (Continued)

```
 186.crafty:  -O3 -funroll-loops -fprefetch-loop-arrays
 197.parser:  -O3 -funroll-all-loops -m32 +FDO
 252.eon:     -O3 -funroll-all-loops -ffast-math -finline-limit=3000 +FDO
 253.perlbmk: -O3 -funroll-all-loops -finline-limit=1800 +FDO
 254.gap:     basepeak=true
 255.vortex:  -O3 -funroll-all-loops -finline-limit=1000 +FDO
 256.bzip2:   -O3 -funroll-all-loops -freduce-all-givs -finline-limit=2700 +FDO
 300.twolf:   -O3 -funroll-all-loops -finline-limit=2000 -m32 +FDO
ONESTEP is used for all base and peak runs
BIOS rev.: 1001
Memory used is Corsair CMX512R-3200C2
Cas Latency manually set to 2.0 in BIOS
```