CINT2000 Result

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Hewlett-Packard Company
ProLiant DL740 (3.0GHz, Intel Xeon MP)

SPECint_rate2000 = NC

SPECint_rate_base2000 = NC

SPEC license #: 3	Tested by: Hewlett-Packard Company	Test date: Mar-2004	Hardware Avail: Feb-2004	Software Avail: Sep-2003

SPEC has determined that this result was not in compliance with the SPEC CPU2000 run and reporting rules. Specifically, the result did not meet the requirement for 4 flags for baseline.

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	NC	NC	8	NC	NC
175.vpr	8	NC	NC	8	NC	NC
176.gcc	8	NC	NC	8	NC	NC
181.mcf	8	NC	NC	8	NC	NC
186.crafty	8	NC	NC	8	NC	NC
197.parser	8	NC	NC	8	NC	NC
252.eon	8	NC	NC	8	NC	NC
253.perlbmk	8	NC	NC	8	NC	NC
254.gap	8	NC	NC	8	NC	NC
255.vortex	8	NC	NC	8	NC	NC
256.bzip2	8	NC	NC	8	NC	NC
300.twolf	8	NC	NC	8	NC	NC

Hardware

CPU:	Intel Xeon MP (3.0GHz, 100MHz bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 8 chips, 1 core/chip
CPU(s) orderable:	1,2,4,8
Parallel:	No
Primary Cache:	12K micro-ops I + 8KBD on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	4096KB(I+D) on chip
Other Cache:	N/A
Memory:	8x1024MB 133MHz SDRAM
Disk Subsystem:	1x18.2GB 15K Ultra320 SCSI
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise (Build 3790)
Compiler:	Intel C++ Compiler 7.1 Build 20030813Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -QxW -Qipo -Oi- shlw32M.lib ONESTEP=yes
Base tuning for C++ programs: -Qipo -GX -GR +FDO ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafy: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:       -QxW -Oi- -Qipo -O3 +FDO ONESTEP=yes
  175.vpr:        -QxW -Qipo -O3 +FDO ONESTEP=yes
  176.gcc:        -QxW -Qipo -Oi- -O3 +FDO shlw32M.lib ONESTEP=yes
```


Non-Compliant

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Notes/Tuning Information (Continued)

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   181.mcf:        -Oa -Qipo +FDO shlw32M.lib ONESTEP=yes
   186.crafty:     -QxW -Qipo -O3 +FDO ONESTEP=yes
   197.parser:     -QxW -Qipo -Oa -O3 +FDO ONESTEP=yes
   252.eon:        -QxW -Qipo -O3 +FDO ONESTEP=yes
   253.perlbmk:    -Qipo -Oa -O3 +FDO shlw32M.lib ONESTEP=yes
   254.gap:        -QxW -Qipo -Oi- -O3 +FDO shlw32M.lib ONESTEP=yes
   255.vortex:     -QxW -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
   256.bzip2:      -Qunroll1 -Oa -Oi- -Qipo +FDO ONESTEP=yes
   300.twolf:      -QxW -Qipo -Oa -Oi- -O3 +FDO shlw32M.lib ONESTEP=yes
 BIOS Configuration Notes
   Hyper Threading disabled

Submitted_by: "Schmidt, David (Performance Eng.)" <d.schmidt@hp.com>
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Non-Compliant