CINT2000 Result

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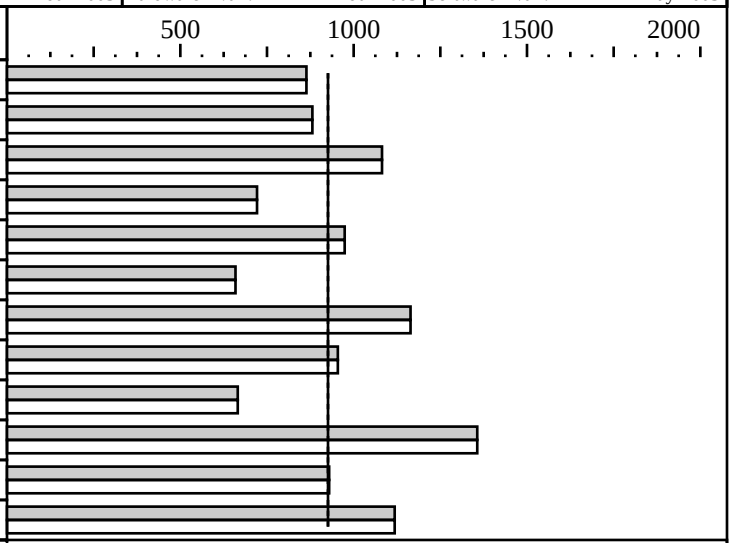
ION
I2X2 (1.4GHz Itanium 2 processor)

SPECint2000 = 926

SPECint_base2000 = 926

SPEC license #: 2697 | Tested by: K.Josephson, ION | Test date: Jul-2003 | Hardware Avail: Jul-2003 | Software Avail: May-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	162	864	162	864
175.vpr	1400	159	881	159	881
176.gcc	1100	102	1082	102	1082
181.mcf	1800	250	721	250	721
186.crafty	1000	103	974	103	974
197.parser	1800	273	659	273	659
252.eon	1300	112	1164	112	1164
253.perlbmk	1800	189	954	189	954
254.gap	1100	165	666	165	666
255.vortex	1900	140	1357	140	1357
256.bzip2	1500	161	929	161	929
300.twolf	3000	268	1118	268	1118



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	4MB (on chip) per CPU
Other Cache:	N/A
Memory:	8 GB (8x1GB DIMMS)
Disk Subsystem:	1x 36GB SCSI (Seagate Cheetah 10k rpm)
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 8
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030605)
File System:	ext2
System State:	Multi-user

Notes/Tuning Information

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Tested by ION Computer Systems
Tests performed with uniprocessor kernel.
Only 1 processor was enabled by operating system.
 +FDO: PASS1=-prof_gen  PASS2=-prof_use

 Baseline optimization flags:
   C programs:       -O3 -ipo +FDO
   C++ programs:     -O2 -ipo -ansi_alias +FDO

 Portability Flags:
   176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
   186.crafty: -DLINUX_i386
   252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
   253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
                -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
   254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
            -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
   255.vortex: -DSPEC_CPU2000_LP64
```

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Notes/Tuning Information (Continued)

Peak flags same as baseline (basepeak=true set globally).