CINT2000 Result

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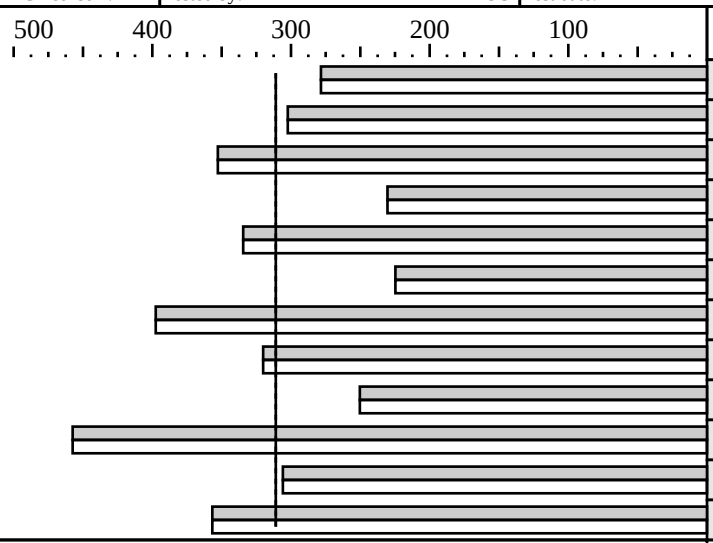
SGI
SGI Altix 3000 (1300MHz, Itanium 2)

SPECint_rate2000 = 311

SPECint_rate_base2000 = 311

SPEC license #: 4 | Tested by: SGI | Test date: Jun-2003 | Hardware Avail: Jun-2003 | Software Avail: Jun-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	32	187	278	32	187	278
175.vpr	32	172	302	32	172	302
176.gcc	32	116	353	32	116	353
181.mcf	32	290	230	32	290	230
186.crafty	32	111	334	32	111	334
197.parser	32	297	225	32	297	225
252.eon	32	121	398	32	121	398
253.perlbmk	32	209	320	32	209	320
254.gap	32	163	250	32	163	250
255.vortex	32	154	457	32	154	457
256.bzip2	32	182	306	32	182	306
300.twolf	32	312	357	32	312	357

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1300
FPU:	Integrated
CPU(s) enabled:	32 cores, 32 chips, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	3.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	64 GB (16*512MB DIMMS per 4cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.2
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030507)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 +FDO -ansi_alias

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```