CINT2000 Result

IBM Corporation

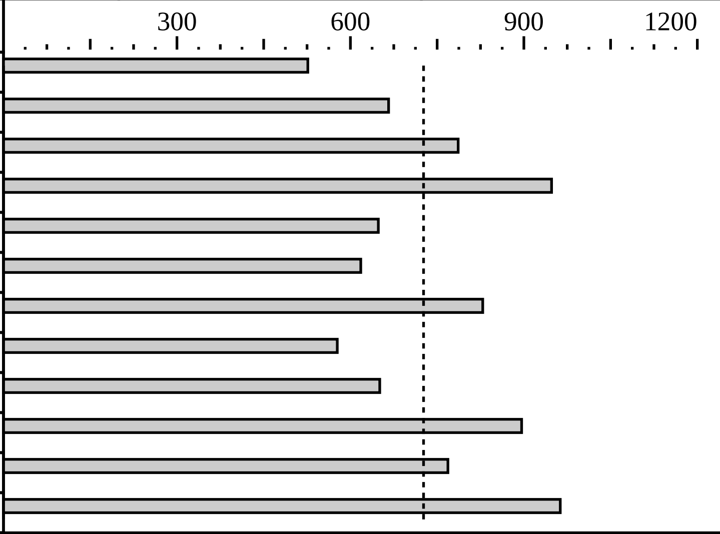
IBM eServer pSeries 615 Model 6C3 (1200 Mhz, 1 CPU)

SPECint2000 = --

SPECint_base2000 = 727

SPEC license #:	11	Tested by:	IBM, Austin, Tx	Test date:	May-2003	Hardware Avail:	Jun-2003	Software Avail:	May-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	266	526		
175.vpr	1400	210	666		
176.gcc	1100	140	786		
181.mcf	1800	190	948		
186.crafty	1000	154	648		
197.parser	1800	291	618		
252.eon	1300	157	829		
253.perlbmk	1800	312	577		
254.gap	1100	169	651		
255.vortex	1900	212	896		
256.bzip2	1500	195	769		
300.twolf	3000	312	963		



Hardware

CPU:	POWER4+
CPU MHz:	1200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 2 cores/chip, 1 processor chip/DCM
CPU(s) orderable:	1,2 (order by # cores)
Parallel:	No
Primary Cache:	64KBI+32KBD (on chip) per core
Secondary Cache:	1536KB unified (on chip) per chip
L3 Cache:	8MB unified (off-chip) per DCM, 1 DCM in SUT
Other Cache:	None
Memory:	8 GB
Disk Subsystem:	1x36GB Ultra3 SCSI 10K RPM
Other Hardware:	None

Software

Operating System:	SLES 8 Service Release 1 for pSeries w/2.4.19 kernel
Compiler:	IBM VisualAge C++ Version 6.0 for Linux on pSeries IBM XL Fortran Version 8.1 for Linux on pSeries
File System:	ext2
System State:	Multi-user

Notes/Tuning Information

```
cfg file:  ppc32-linux-ibm-ref-o5.cfg

Compiled 32-bit applications

CC  = /opt/ibmcmp/vac/6.0/bin/cc
CXX = /opt/ibmcmp/vacpp/6.0/bin/xlC



DCM: Acronym for "Dual-chip module"
SUT: Acronym for "System under test"

IBM eServer pSeries 615 Model 6C3 (rack-mount server) and pSeries
615 Model 6E3 (deskside server) are electronically equivalent.
1 processor was deconfigured through the configuration menu.
SLES: SuSE Linux Enterprise Server
Integer optimization flags
   Int: -qpdf1/pdf2 -O5
```

Notes/Tuning Information (Continued)

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Integer portability flags
   gcc:  -DHOST_WORDS_BIG_ENDIAN
   mcf: None
   crafty: -DLINUX_PPC32
   parser: None
   eon: -DHAS_ERRLIST
   eon:
   eon: Using srcalt=fmax_errno

   perlbmk: -DSPEC_CPU2000_LINUX_PPC32 -DSPEC_CPU2000_NEED_BOOL
            -DHAS_FGETPOS -DHAS_FSETPOS
   gap: -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_CALLOC_PROTO
   vortex: None
   bzip2: None
   twolf: -DHAVE_SIGNED_CHAR
```