CINT2000 Result

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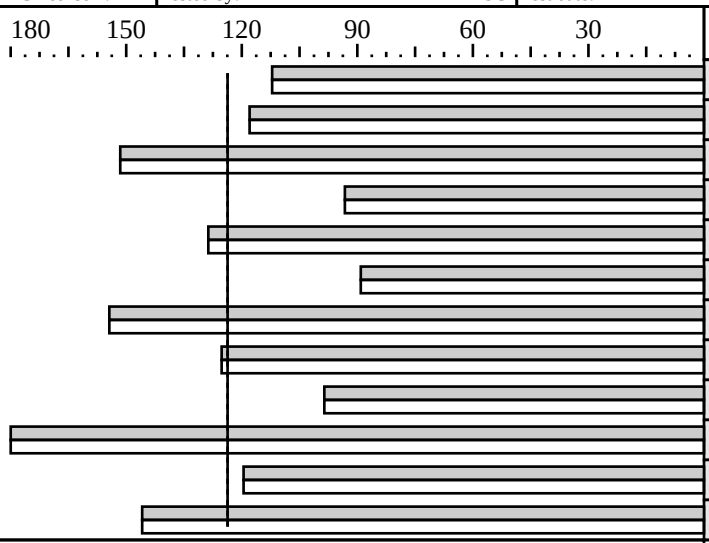
SGI
SGI Altix 3000 (1000MHz, Itanium 2)

SPECint_rate2000 = 124

SPECint_rate_base2000 = 124

SPEC license #: 4 | Tested by: SGI | Test date: Apr-2003 | Hardware Avail: Feb-2003 | Software Avail: Apr-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	16	232	112	16	232	112
175.vpr	16	220	118	16	220	118
176.gcc	16	135	152	16	135	152
181.mcf	16	358	93.2	16	358	93.2
186.crafty	16	144	129	16	144	129
197.parser	16	375	89.1	16	375	89.1
252.eon	16	156	154	16	156	154
253.perlbmk	16	267	125	16	267	125
254.gap	16	207	98.6	16	207	98.6
255.vortex	16	196	180	16	196	180
256.bzip2	16	233	120	16	233	120
300.twolf	16	382	146	16	382	146

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1000
FPU:	Integrated
CPU(s) enabled:	16 cores, 16 chips, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	3.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	32 GB (16*512MB DIMMS per 4cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.1
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030327)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 -ansi_alias +FDO

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```