CINT2000 Result

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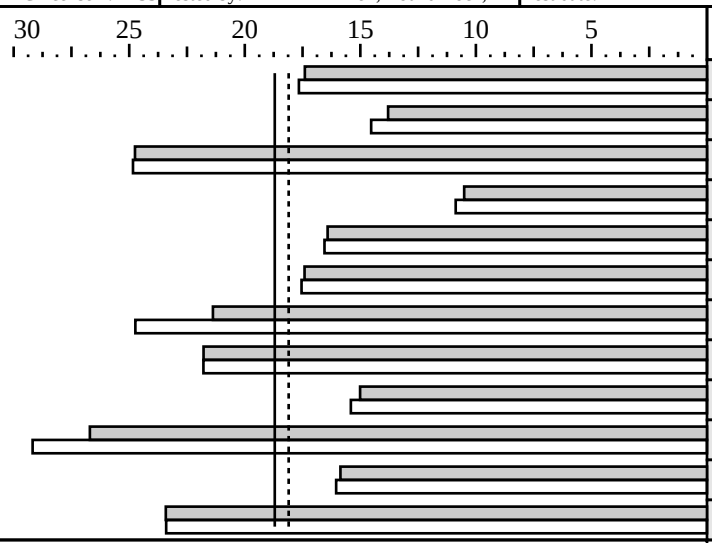
Dell
PowerEdge 6650 (2.0 GHz Xeon MP)

SPECint_rate2000 = 18.7

SPECint_rate_base2000 = 18.1

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Sep-2002 | Hardware Avail: Nov-2002 | Software Avail: Jun-2001



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	187	17.4	2	184	17.7
175.vpr	2	235	13.8	2	224	14.5
176.gcc	2	103	24.8	2	103	24.8
181.mcf	2	397	10.5	2	384	10.9
186.crafty	2	141	16.4	2	140	16.6
197.parser	2	240	17.4	2	238	17.5
252.eon	2	141	21.4	2	122	24.7
253.perlbmk	2	192	21.8	2	192	21.8
254.gap	2	170	15.0	2	166	15.4
255.vortex	2	165	26.7	2	151	29.2
256.bzip2	2	219	15.9	2	217	16.0
300.twolf	2	297	23.4	2	297	23.4

Hardware

CPU:	Intel Xeon (400 MHz system bus)
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1, 2, 4
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	2048KB(I+D) on chip
Other Cache:	None
Memory:	8 x 512MB DDR200
Disk Subsystem:	1 x 36GB SEAGATE ST336706LC
Other Hardware:	None

Software

Operating System:	Windows 2000 Advanced Server (SP2)
Compiler:	Intel C++ Compiler 5.0.1 (020125Z) Microsoft Visual C++ 6.0 (SP5) MicroQuill SmartHeap Library 5.0
File System:	NTFS
System State:	Hyper-Threading Technology disabled in BIOS

Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK-DIRECTED OPTIMIZATION
  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use
BASE TUNING
  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR
PEAK TUNING
  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
```

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Notes/Tuning Information (Continued)

```
    255.vortex:  -Qwp_ipo +FDO -QxW -Oa
    256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
    300.twolf:   -Qipo +FDO -QxW shlW32M.lib
  EXTRA LIBRARIES
    shlW32M.lib: MicroQuill SmartHeap Library 5.0
                 www.microquill.com
```