



CFP2000 Result

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Advanced Micro Devices

ASUS A7N8X (REV 1.02) Motherboard, AMD Athlon (TM) XP 2800+

SPECfp2000 = 843

SPECfp_base2000 = 782

SPEC license #: 49 Tested by: AMD Sunnyvale, CA Test date: Sep-2002 Hardware Avail: Dec-2002 Software Avail: Sep-2002

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	140	1146	140	1145	
171.swim	3100	249	1246	249	1246	
172.mgrid	1800	224	805	224	805	
173.applu	2100	300	701	296	708	
177.mesa	1400	116	1209	113	1235	
178.galgel	2900	407	713	235	1236	
179.art	2600	473	549	459	566	
183.quake	1300	180	723	148	877	
187.facerec	1900	213	893	213	893	
188.amp	2200	409	537	381	577	
189.lucas	2000	242	827	241	830	
191.fma3d	2100	242	866	243	866	
200.sixtrack	1100	193	569	188	585	
301.apsi	2600	437	595	381	682	

Hardware

CPU: AMD Athlon (TM) XP 2800+
CPU MHz: 2250
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 256KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 2x256MB PC2700 DDR SDRAM CL2.5
Disk Subsystem: Western Digital WD1200
Other Hardware: None

Software

Operating System: Windows XP Pro, SP 1
Compiler: Intel C++ 5.0.1 build 020125Z
Intel Fortran 5.0.1 build 020125Z
Compaq Visual Fortran 6.6
Microsoft Visual Studio 6.0 SP5 (libraries)
MicroQuill Smartheap Library 5.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
icl and ifl are the Intel C++ and Fortran compilers
f90 is the Compaq Fortran compiler
shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
Portability:
178.galgel: -FI -Fe$@ -link -stack:32000000
Baseline: C          icl -QxK -Qipo +FDO shlw32M.lib
Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
Peak tuning:
168.wupwise:  ifl -O3 -QxK -Qwp_ipo +FDO
171.swim:     ifl -O3 -QxK -Qwp_ipo +FDO
172.mgrid:   ifl -O3 -QxK -Qwp_ipo +FDO
173.applu:   ifl -O3 -QxK -Qwp_ipo -Qscalar_rep- -Qauto +FDO
177.mesa:    icl -O3 -QxK -Qwp_ipo -Oa +FDO shlw32M.lib
178.galgel:  f90 -Optimize:5 -fast
179.art:     icl -O3 -QxK -QaxW -Qwp_ipo -Oa +FDO shlw32M.lib
183.quake:   icl -O3 -QxK -Qwp_ipo -Qrcd -Oa +FDO shlw32M.lib
```



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SPECfp2000 = **843**

SPECfp_base2000 = **782**

SPEC license #: 49 | Tested by: AMD Sunnyvale, CA | Test date: Sep-2002 | Hardware Avail: Dec-2002 | Software Avail: Sep-2002

Notes/Tuning Information (Continued)

```
187.facerec: ifl -O3 -QxK -Qipo +FDO
188.ammf:    icl -O3 -QxK -Qwp_ipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qwp_ipo +FDO shlw32M.lib
191.fma3d:   ifl -O3 -QxK -Qwp_ipo +FDO
200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlw32M.lib LIBC.lib libirc.lib OLDNAMES.lib
ONESTEP is used for all base and peak runs
```

The tested system can be assembled using an ATX case such as the Antec KS-282, a 300W power supply such as the Sparkle FSP300-60GT, and a PCI or AGP video card. The System bus runs at 333MHz. The two SDRAMs were used in a dual-channel configuration.