## CINT2000 Result

**Hewlett-Packard Company**  
*hp workstation zx2000 (900 MHz, Itanium 2)*  

**SPECint2000 =**  
**SPECint_base2000 =** 668

### Hardware
- **CPU:** Intel Itanium 2  
- **CPU MHz:** 900  
- **FPU:** Integrated  
- **CPU(s) enabled:** 1 core, 1 chip, 1 core/chip  
- **CPU(s) orderable:** 1  
- **Parallel:** no  
- **Primary Cache:** 16KB + 16KBD (on chip) per CPU  
- **Secondary Cache:** 256KB (on chip) per CPU  
- **L3 Cache:** 1.5 MB (on chip) per CPU  
- **Other Cache:** N/A  
- **Memory:** 2GB (4 * 512MB DIMMs)  
- **Disk Subsystem:** 1x36GB SCSI disk  
- **Other Hardware:** N/A

### Software
- **Operating System:** HPUX11i-TCOE B.11.22  
- **Compiler:** HP C/ANSI C Compiler B.11.22  
  - HP aC++ Compiler B.11.22  
  - PHSS_27286: C product  
  - PHSS_27284: C++ product  
  - PHSS_27285: C++ library  
  - PHSS_27287: U2comp  
  - PHSS_27288: Linker  
  - PHSS_27291: PBO  
  - PHSS_27293: milli.a  
  - Caliper v2.1 www.hp.com/go/caliper  
- **File System:** VxFS  
- **System State:** Multi-user

### Notes/Tuning Information

#### Portability Flags:
- 176.gcc : -DHOST_WORDS_BIG_ENDIAN  
- 181.mcf : -DWANT_STDC_PROTO  
- 186.crafty : -DHP  
- 252.eon : -I. -DFMAX_IS_DOUBLE -DNDEBUG  
- 253.perlbmk : -DSPEC_CPU2000_HP  
- 254.gap : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

#### Base Flags:
- all : +Oprofile=collect/+Oprofile=use  
- C : +Ofaster -exec -Wl,-archive_shared  
- C++ : +Ofaster +inline_level 2 -minshared

### Kernel Tunables:

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org
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Notes/Tuning Information (Continued)

maxdsiz 0x7b03a000
maxdsiz_64bit 0x04000000000
maxssiz 0x10000000
maxssiz_64bit 0x40000000
vps_ceiling 16384
vps_pagesize 4096
swapmem_on 0
dbc_max_pct 20
dbc_min_pct 20